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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **TAKAYAMA, Toshio, et al.**

Group Art Unit: **2811**

Serial No.: **10/735,886**

Examiner: **Nitin PAREKH**

Filed: **December 16, 2003**

P.T.O. Confirmation No.: **5693**

For: **SEMICONDUCTOR DEVICE HAVING A MULTILAYER  
INTERCONNECTION STRUCTURE AND FABRICATION  
PROCESS THEREOF**

**PETITION FOR EXTENSION OF TIME**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Date: April 24, 2006

Sir:

Applicants petition the Commissioner for Patents to extend the time for response to the Office Action dated December 23, 2005 for one month, from March 23, 2006 to April 24, 2006 (April 23, 2006 being a Sunday).

Attached please find a check in the amount of \$120.00 to cover the cost of the extension for a large entity. In the event that any additional fees are due in connection with this paper, please charge our Deposit Account No. 01-2340.

04/25/2006 EAYALEW1 00000092 10735886

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120.00 OP

Respectfully submitted,

ARMSTRONG, KRATZ, QUINTOS,  
HANSON & BROOKS, LLP

*[Handwritten signature of Mel R. Quintos]*  
Mel R. Quintos

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PATENT TRADEMARK OFFICE